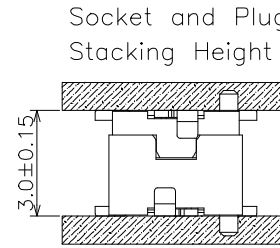
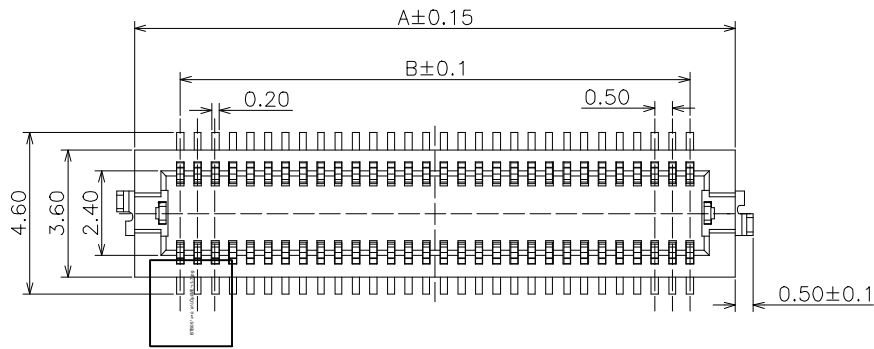
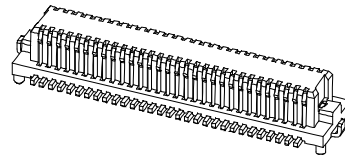
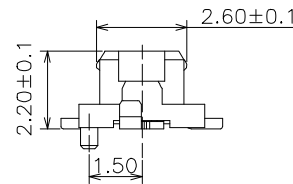
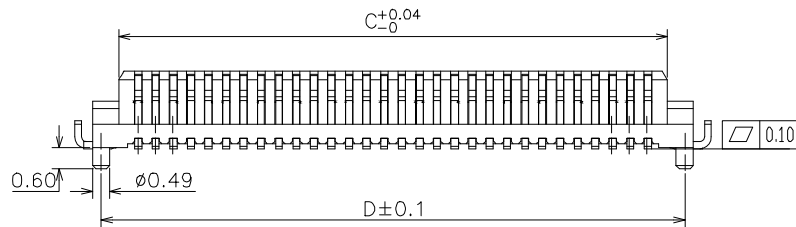


\*All raw material,production process,electroplating must meet the requirements of EU RoHS.\*

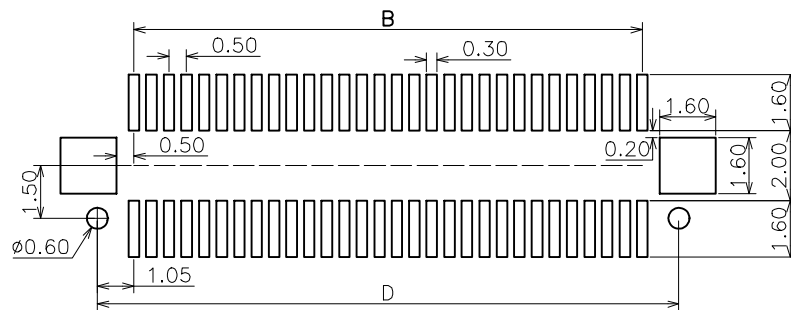
REV.	ECN NO.	LOCAS.	DESCRIPTION	DATE	DESIGN
A0			RELEASE TO CUSTOMER	200218	HANG



Note:  
 Electrical  
 Current Rating:0.5A max  
 Contact Resistance:50mΩ max(Initial),75mΩ max(After)  
 Dielectric Withstanding Voltage:150VAC  
 Insulation Resistance 500mega ohms min. at 100 VDC  
 Physical  
 Housing:thermoplastic, UL 94V-0 rated,inWhite Color  
 Contact: Copper alloy  
 Operating Temperature:-40°C to +85°C



	A	B	C	D
10 PIN	4.60	2.00	3.10	4.10
12 PIN	5.10	2.50	3.60	4.60
14 PIN	5.60	3.00	4.10	5.10
20 PIN	7.10	4.50	5.60	6.60
22 PIN	7.60	5.00	6.10	7.10
24 PIN	8.10	5.50	6.60	7.60
30 PIN	9.60	7.00	8.10	9.10
32 PIN	10.10	7.50	8.60	9.60
36 PIN	11.10	8.50	9.60	10.60
40 PIN	12.10	9.50	10.60	11.60
50 PIN	14.60	12.00	13.10	14.10
60 PIN	17.10	14.50	15.60	16.60
80 PIN	22.10	19.50	20.60	21.60



PCB Layout

DIM	TOL	DIM	TOL
X.	±0.30	X'	±4.0'
X.X	±0.25	X.X'	±3.0'
X.XX	±0.20	X.XX'	±2.0'
X.XXX	±0.10	X.XXX'	±1.0'

## 深圳朗思达科技有限公司

DRAW NO.		DATE		DRAW NAME:	
DESIGN	HANG	20200218	Board To Board Pitch0.5mm Plug带接地片 合高3.0 单槽		
CHECK	ZHAOZZ	20200218	P/N NO. BTB-051-301-XXDF12		CIB CODE:
APPROVED	RYAN	20200218	UNIT: mm	SHEET: N/A	MODEL:
			REV. A0	SCALE N/A	

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